

KOREAN PATENT ABSTRACTS

(11)Publication number: 1020020020545 A
(43)Date of publication of application: 15.03.2002

(21)Application number: 1020000053781
(22)Date of filing: 09.09.2000
(51)Int. Cl. H04L 27/26

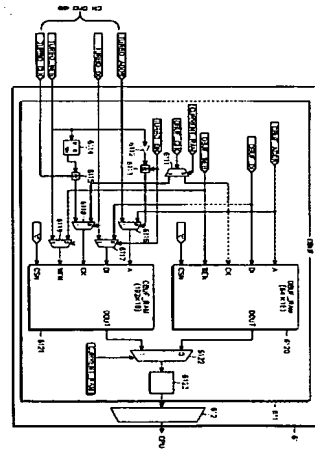
(71)Applicant: SAMSUNG ELECTRONICS CO., LTD.
(72)Inventor: KIM, YONG CHAN

(54) PARTIAL SHARE OUTPUT BUFFER OF VITERBI/TURBO DECODER FOR INTERNATIONAL MOBILE TELECOMMUNICATIONS 2000 MODEM

(57) Abstract:

PURPOSE: A partial share output buffer of a Viterbi/turbo decoder for an international mobile telecommunications(IMT) 2000 modem is provided to prevent wastes of an RAM having 2¹⁶ size and 16 bit register by using it in a channel decoder of an IMT 2000 terminal.

CONSTITUTION: An output buffer block(611) includes a demultiplexer (6111), an inverter(6112), an inverter(6112), a first AND gate(6113), a flip-flop(6114), a second AND gate(6115), and four multiplexers (6116,6117,6118,6119). The demultiplexer(6111) generates a control signal. The demultiplexer(6111) enables/disables clock signals of a fundamental output buffer RAM(6120) and a supplemental buffer RAM (6121). The fundamental output buffer RAM(6120) stores a forward fundamental channel frame and a forward dedicated control channel frame. The supplemental buffer RAM(6121) stores forward supplemental channel frame data at an IS-95C mode and forward supplemental code channel frame data at an IS-95B. A multiplexer(6122) selects an RAM data output. 16 bit register(6123) stores the RAM data output value.



COPYRIGHT KIPO 2002

Legal Status

Date of final disposal of an application (20020531)

Patent registration number (1003424970000)

Date of registration (20020618)

Number of opposition against the grant of a patent ()

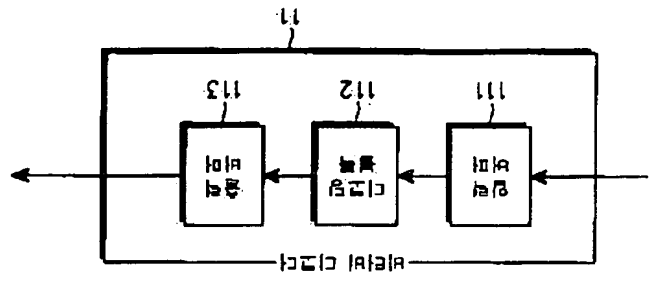
Date of opposition against the grant of a patent (00000000)

Number of trial against decision to refuse ()

Date of requesting trial against decision to refuse ()

Date of extinction of right ()

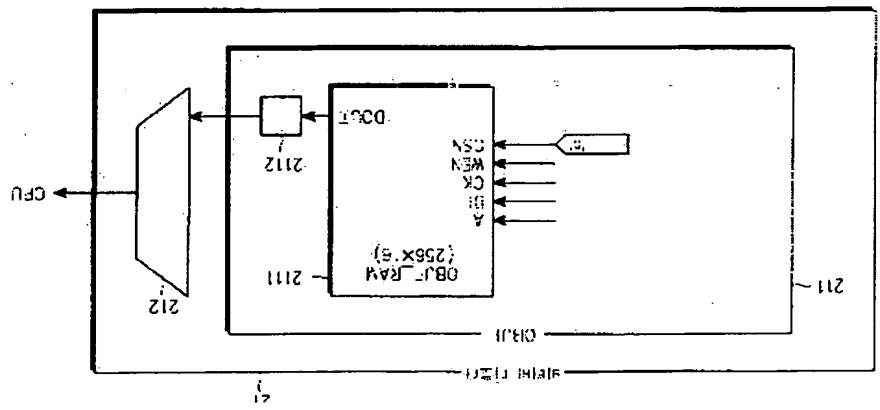
FIG. 1



11-5

10-0342497

FIG. 2



11-6

10-0342497

505

